

Notice of Allowability

Application No.

10/030,252

Examiner

Kumiko C. Koyama

Applicant(s)

YOSHIMOTO ET AL.

Art Unit

2876

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amendment filed on 08 February 2006.
2. ☒ The allowed claim(s) is/are 1-19.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 0904
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Amendment received on February 08, 2005 has been acknowledged.

Allowable Subject Matter

1. Claims 1-19 are allowed.
2. The following is an examiner's statement of reasons for allowance:

Dreifus teaches a portable electronic transaction device, which is an electronic device whose components are encapsulated in a plastic body in the form of a flat card, like a conventional plastic credit card. The device includes means for receiving power transmitted optically from the terminal, and therefore, the card is supplied with power from the outside in a contactless manner. The device also includes means for transmitting and receiving information to and from a terminal and an integrated circuit that comprises a communication buffer, direct memory access (DMA) circuitry, a read only memory (ROM), and central processing unit (CPU). Dreifus also teaches an interrupt control unit. Dreifus discloses that since the direct memory access (DMA) circuit is connected to the RAM and to the interrupt control circuit and since the interrupt control circuit is in turn connected to the time/date clock and the communication buffer, DMA circuit allows the RAM to receive information directly from the time/date clock and to transmit and receive the data from the terminal, via the communication buffer and the interrupt control unit, without relying on the operation of the central processing unit.

Kato discloses a portable terminal that is connected to the external computer apparatus. The switching control circuit in the ASIC outputs the switching signal in

Art Unit: 2876

response to the power source signal, which is input from the external computer apparatus. When the control signal switching circuit receives the switching signal, the control signal switching circuit switches the address bus, the data bus and the memory control signal line from the CPU, to the address bus, the data bus and the memory control signal line from the external computer apparatus. As a result, the access from the CPU to the data storage memory is halted so that it can be access from the external computer apparatus. Kato further teaches that the access from the external computer apparatus to the data storage memory is halted so that it can be access from the CPU, when the user removes the portable terminal from the external computer apparatus. As shown in Fig. 6, the switching control circuit is in connection with memory units ROM and RAM.

However, Dreifus does not teach a state control means for halting the write and read processing on the buffer memory and the nonvolatile memory of the CPU while the transmission circuit is sending/receiving data to/from the outside, and Kato discloses that programs stored in the ROM, which is a non-volatile memory, is read even while the switching control circuit halts access from the CPU, and therefore, Kato does not teach halting the write and read processing on both the buffer memory and the nonvolatile memory. Subsequently, the reason for allowance, in conjunction with other limitations recited in the claims, is the inclusion of halting the write and read processing on the buffer memory and the nonvolatile memory of the CPU while the transmission circuit is sending/receiving data to/from the outside.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Art Unit: 2876

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kumiko C. Koyama whose telephone number is 571-272-2394. The examiner can normally be reached on Monday-Friday 8am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G. Lee can be reached on 571-272-2398. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kumiko C. Koyama
Kumiko C. Koyama
March 06, 2006



THIEN M. LE
PRIMARY EXAMINER